

EMR-based comparison of Cascaded H-Bridge and conventional inverter for EV traction chain

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Cascaded H-Bridge structure



EMR and modeling



Simulation results on efficiency



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Cascaded H-Bridge structure

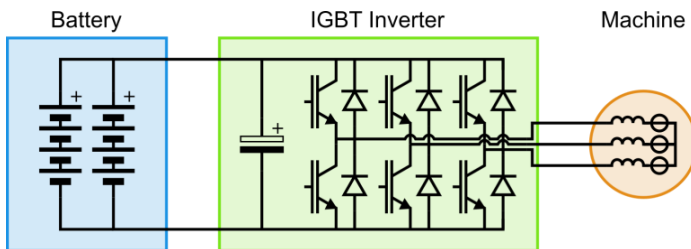
EMR-based comparison of CHB and conventional inverter for EV

EV Traction Chain

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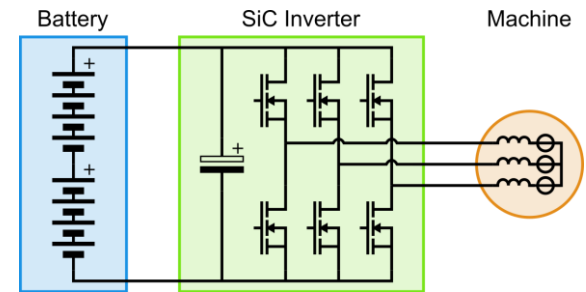
4

Nowadays

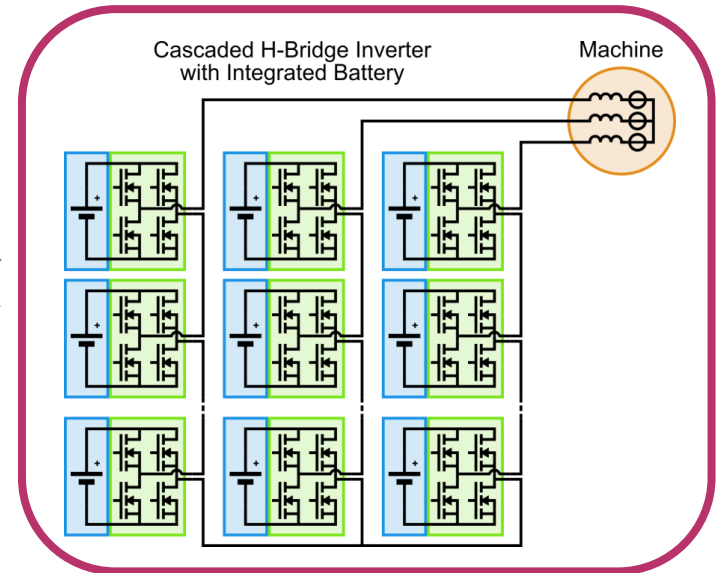


Higher voltage
SiC MOSFET

In the Future



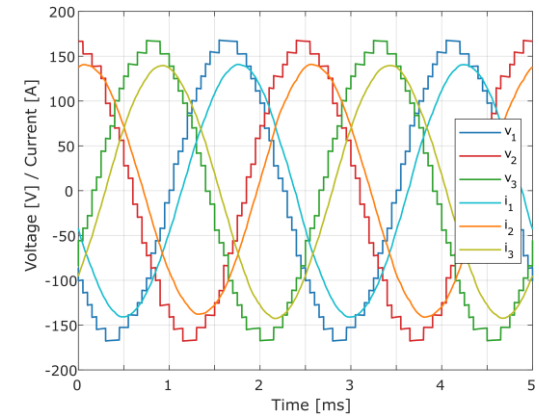
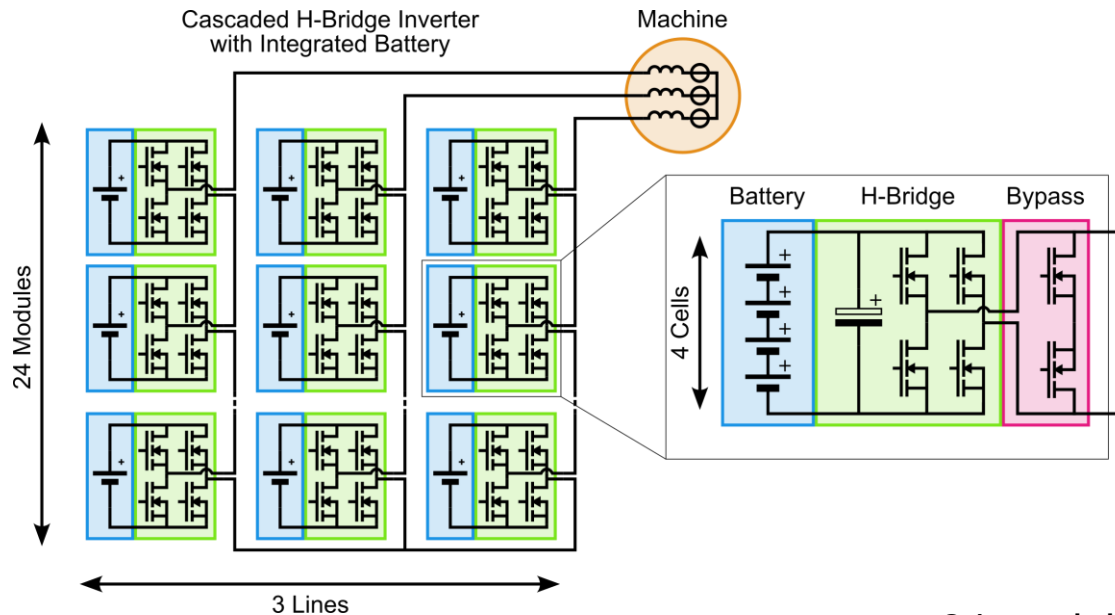
Multilevel Inverter
Lower voltage
Si MOSFET



Cascaded H-Bridge Inverter

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5



24 modules of 4 battery cells per line

Low voltage MOSFETs (40 V)

→ Low $R_{DS\ on}$ ($< 0.5\ m\Omega$)

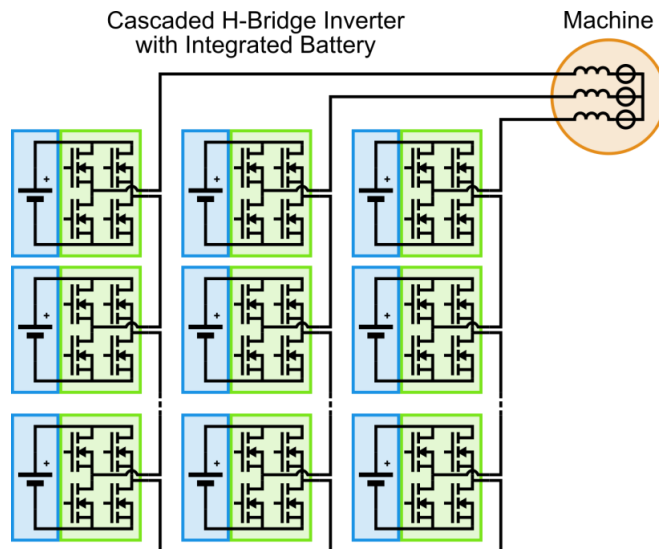
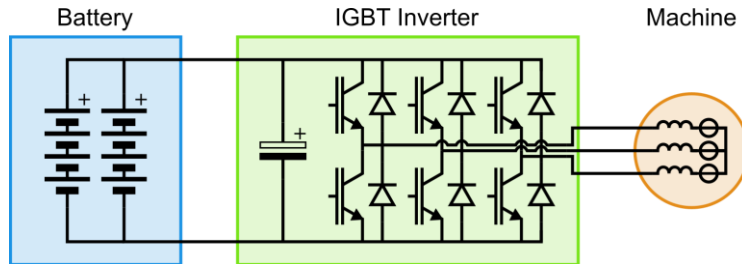
Nearest Level Command (no PWM)

→ Few switching

AC and DC charging without external converter

Fault tolerance





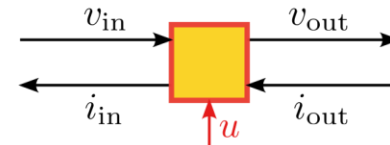
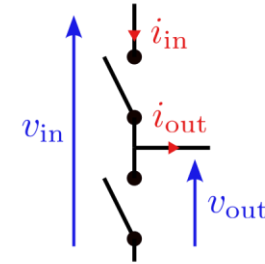
1. Representation and control
 - EMR
 - Modular
 - Compact
 - Numerous freedom degrees
2. Energy management
 - Cell balancing
3. Efficiency estimation
4. Comparison with IGBT and SiC inverters
 - Operating point
 - Realistic conditions



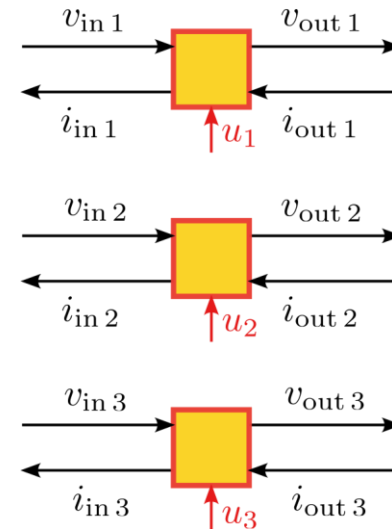
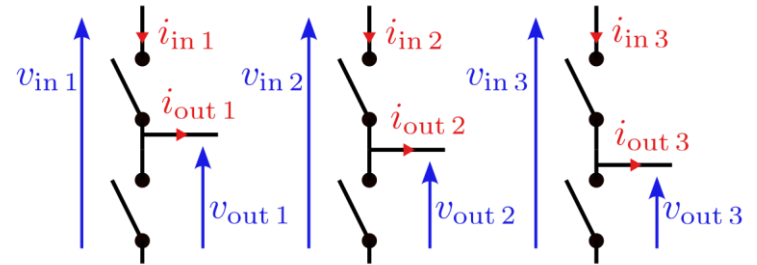
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EMR and modeling

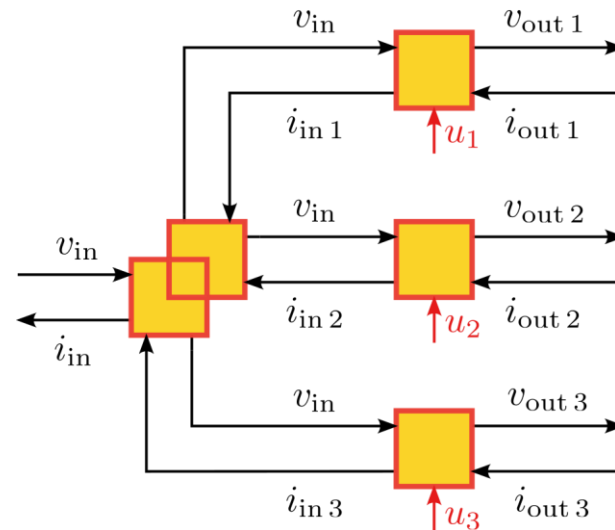
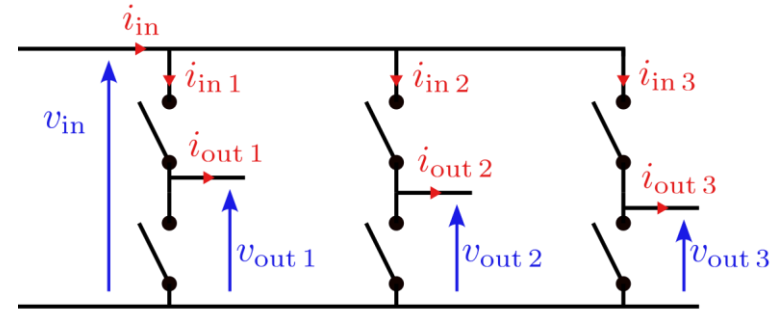
1. One Half-bridge



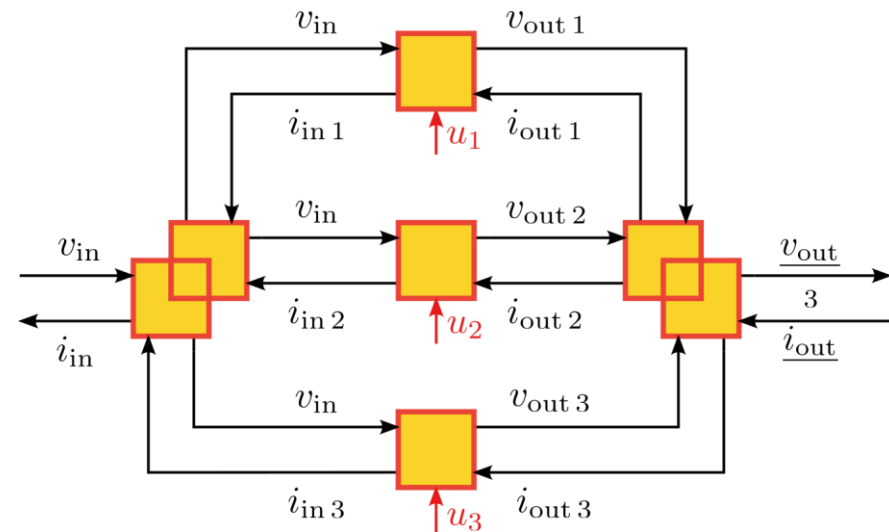
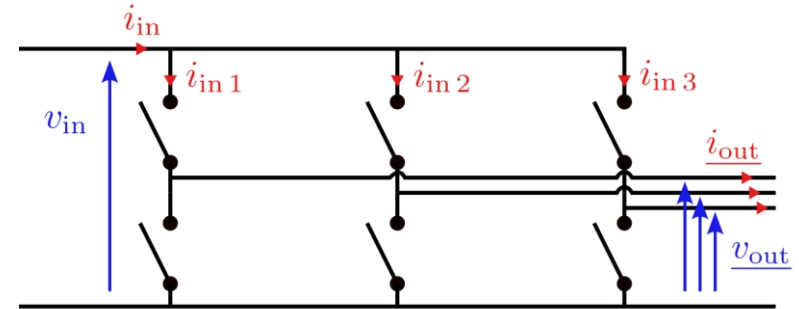
1. One Half-bridge
2. Three Half-bridges



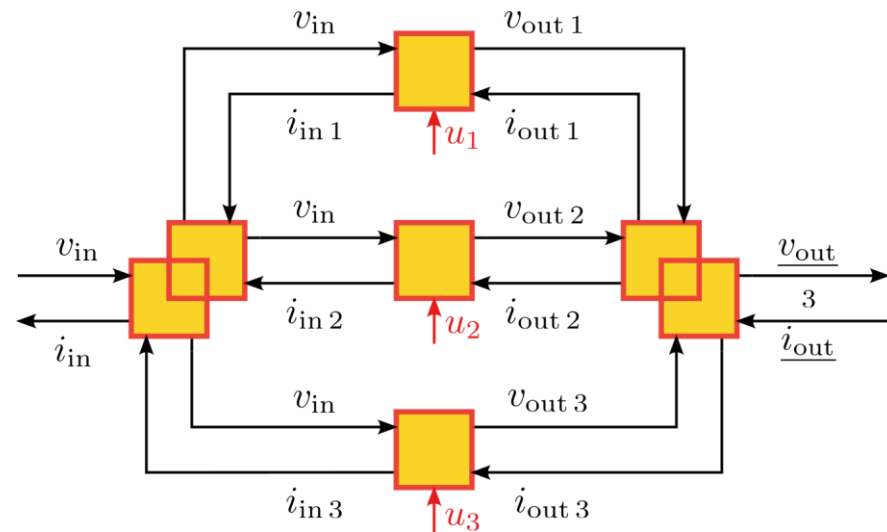
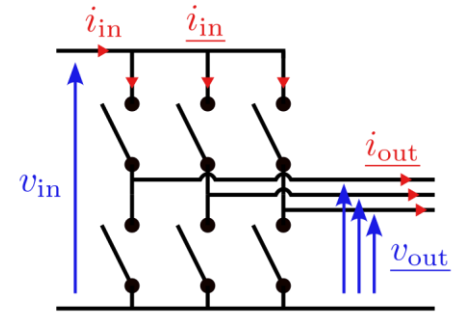
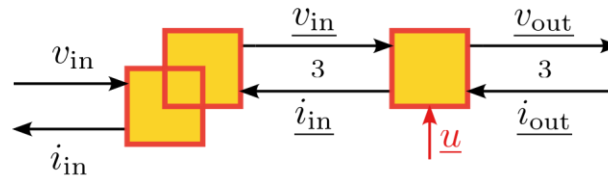
1. One Half-bridge
2. Three Half-bridges
3. Parallel connection



1. One Half-bridge
2. Three Half-bridges
3. Parallel connection
4. Output vectorization



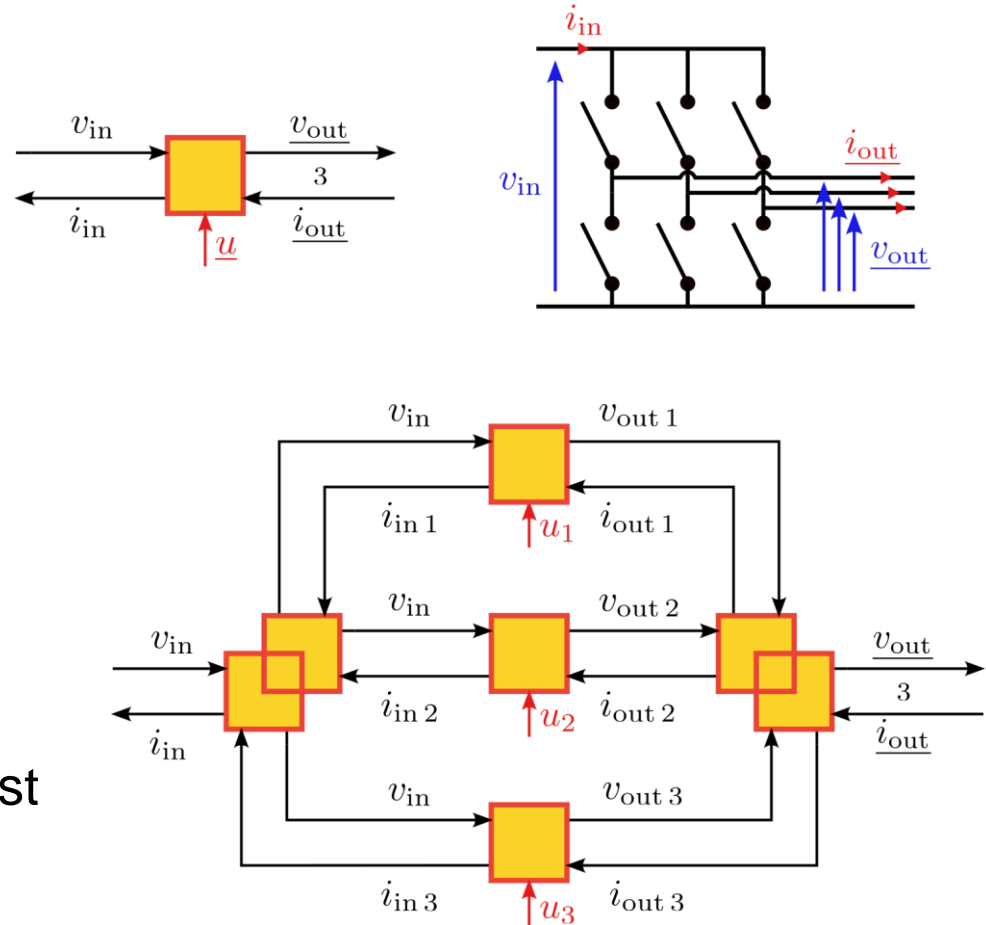
1. One Half-bridge
2. Three Half-bridges
3. Parallel connection
4. Output vectorization
5. Half-bridges vectorization



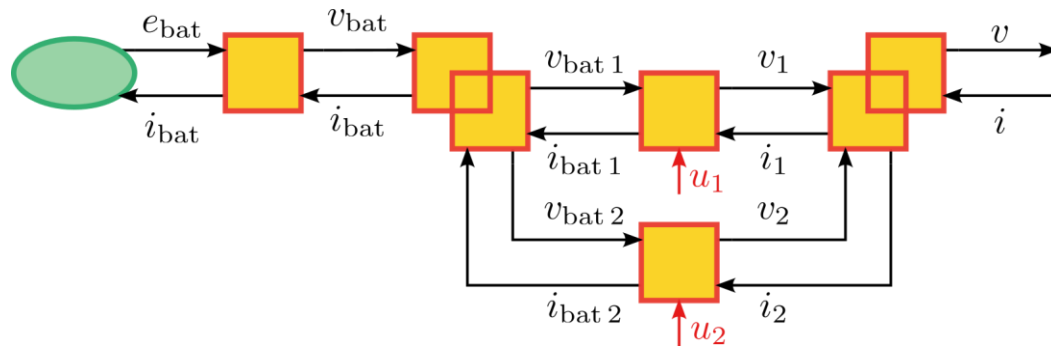
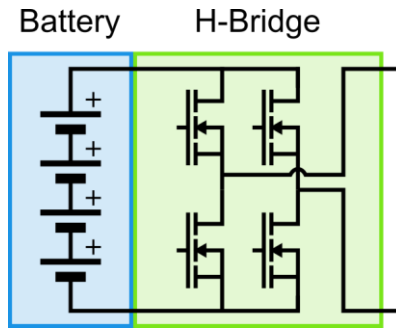
1. One Half-bridge
2. Three Half-bridges
3. Parallel connection
4. Output vectorization
5. Half-bridges vectorization
6. Implicit parallel coupling

Several levels of modelling and vectorization

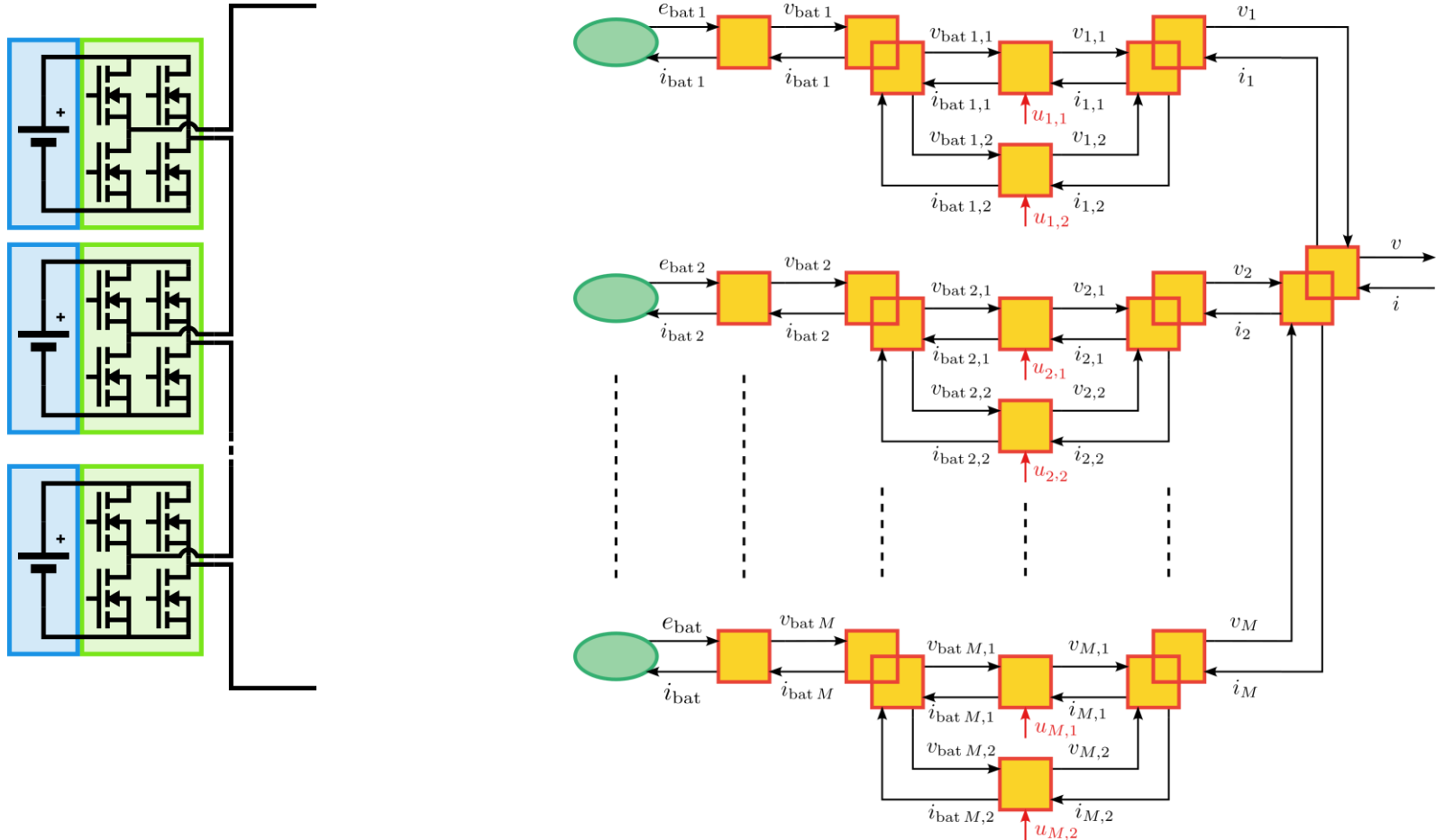
Representation depends on the most important root element



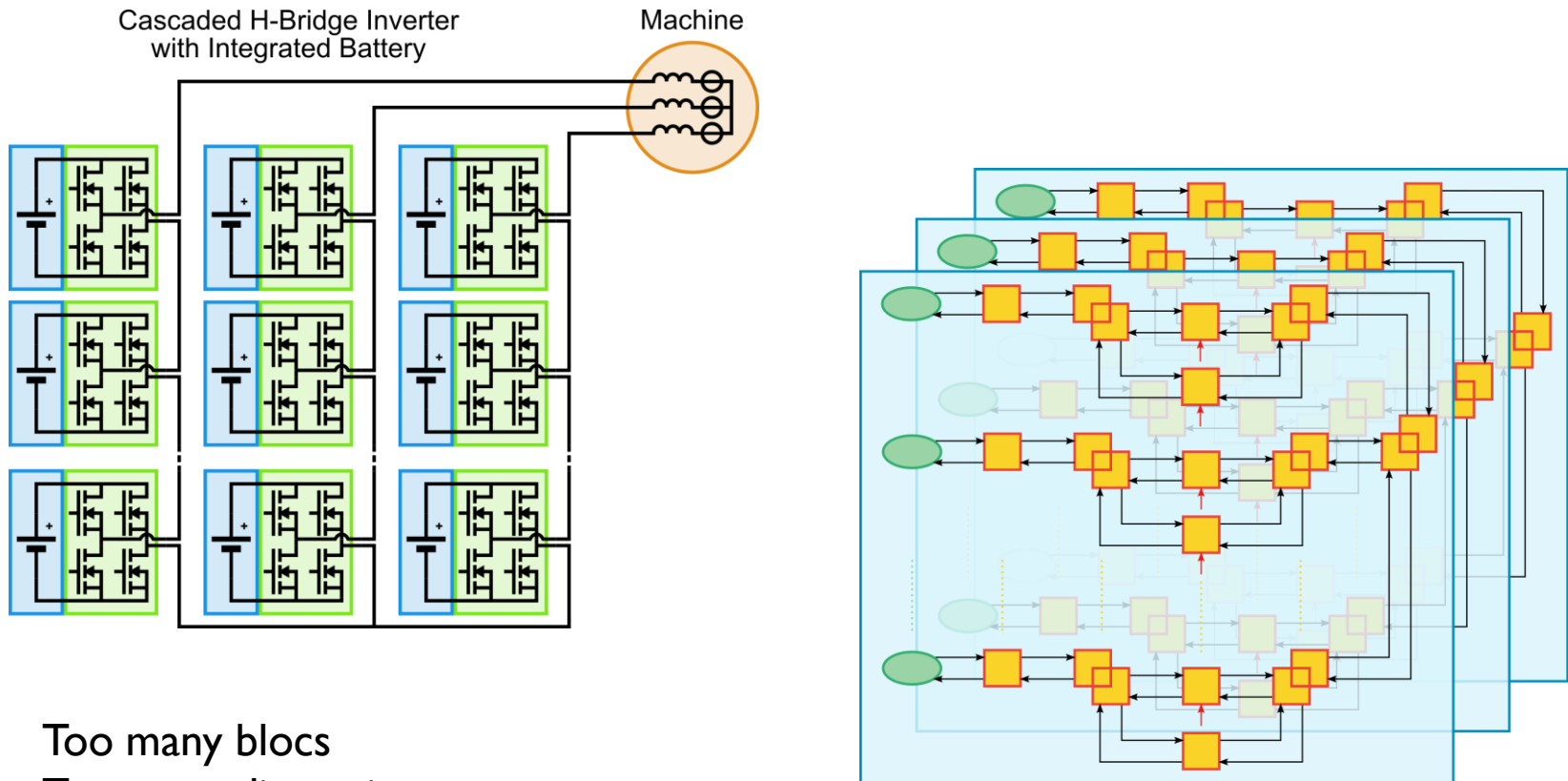
Battery and H-Bridge



Cascaded H-Bridges

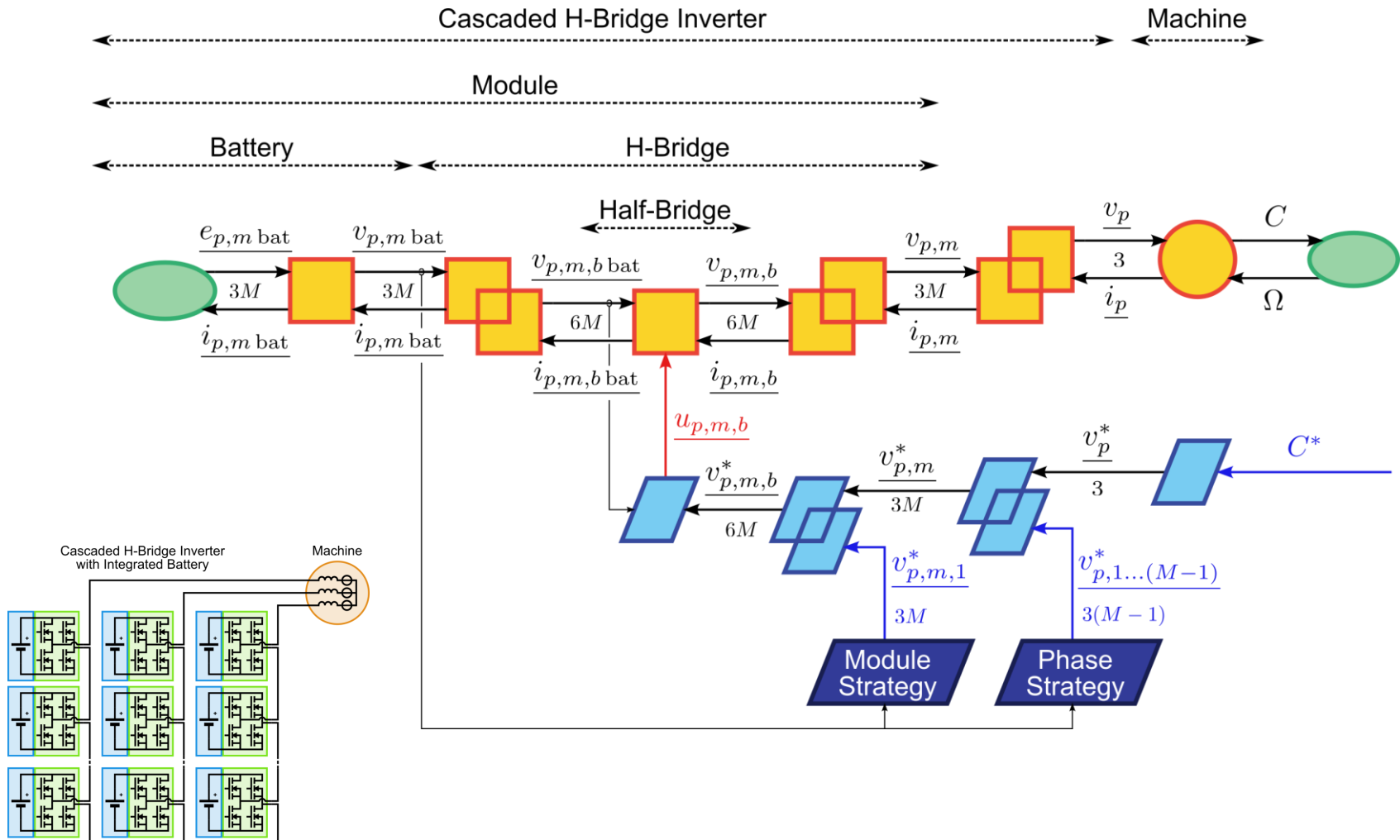


Cascaded H-Bridge Inverter



Too many blocs
Too many dimensions
Too many control inputs
→ Need for vectorization

Cascaded H-Bridge Inverter





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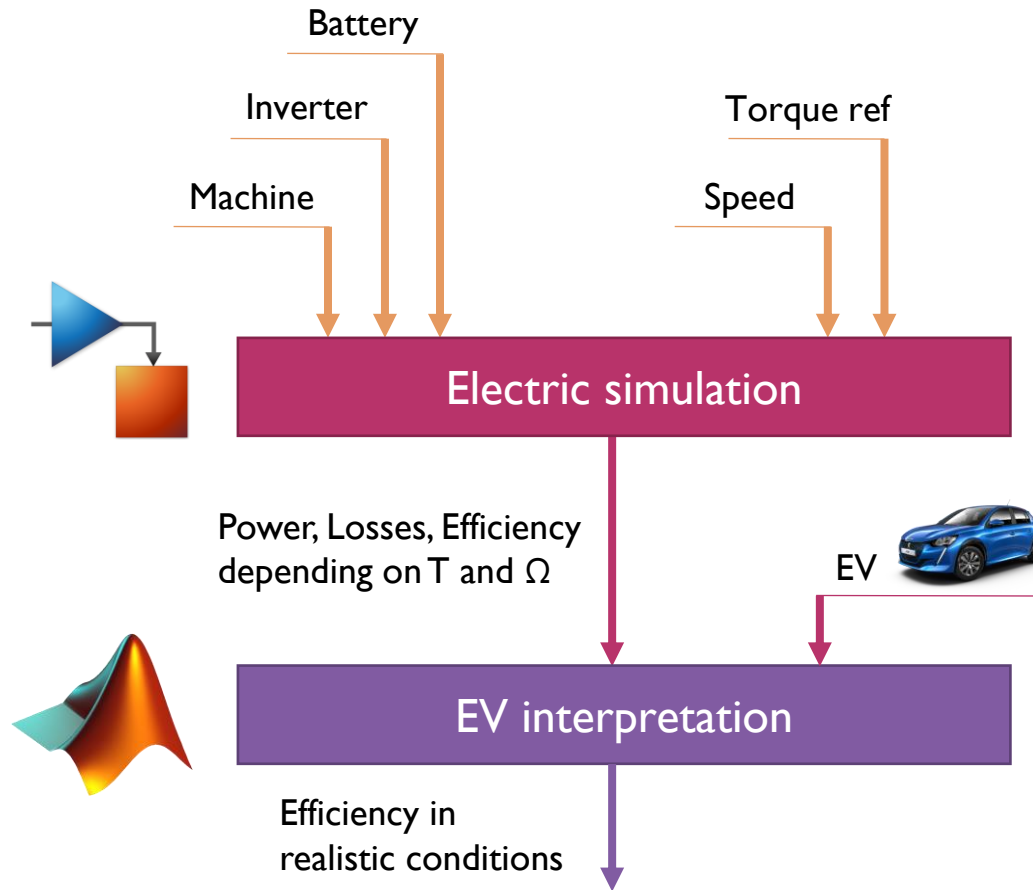
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Workflow

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Thank you !

It's time for questions